



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

WILLIAM P. STEARNS ET AL.

Serial No. 09/678,318 (TI-25833.1)

Filed October 3, 2000

For: OPTIMIZED CIRCUIT DESIGN LAYOUT FOR HIGH
PERFORMANCE BALL GRID ARRAY PACKAGES

Art Unit 2815

Examiner P. Brock, II

Commissioner for Patents
Washington, D. C. 20231.

Sir:

AMENDMENT UNDER 37 C.F.R. 1.111

In response to the Office action dated October 4, 2001, the following remarks are presented:

REMARKS

Claims 1 to 8 and 19 to 28 are resubmitted in unamended form, it being noted that claims 19 and 28 have been indicated to be allowable.

Claims 1, 2, 20 and 21 were rejected under 35 U.S.C. 102(a) as being anticipated by Ohsawa (U.S. 2001/001491 A1). The rejection is respectfully traversed.

Claim 1 requires, among other features, the step of providing a plurality of pairs of traces on the surface, each trace of each of the pairs of traces extending to a different one of the ball pads and extending to ball pads on a plurality of the rows and columns, each trace of each of the pair of

#13 / response
1-3-02
Date

1630

RECEIVED
DEC 28 2001
TECHNOLOGY CENTER 2800